

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of
Inventors: Homayoun Talieh et al.
Appln. No.: 09/941,360
Filed: August 28, 2001
For: METHOD AND APPARATUS FOR PLATING AND
POLISHING A SEMICONDUCTOR SUBSTRATE

Atty. Dkt.: 042496-0269250
Group Art Unit: 1742
Examiner: D.R. Valentine

Date: August 12, 2003

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed herewith is Form PTO-1449 and the reference cited therein. The document listed thereon was cited in the European Examination Report (also enclosed) which was mailed to Applicants by the European Patent Office on July 15, 2003 for the PCT counterpart application. Please see the report for the degree of relevance (category) of this document and for the particular passages cited therefor.

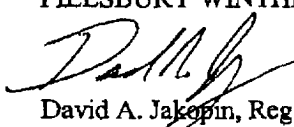
This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

Contingent Petition Under Rule 97(d): Should a Final Rejection or Notice of Allowance issue with a mailing date which precedes or is the same as the filing date of this IDS, please consider this a Petition under Rule 97(d), charge the petition fee to our Deposit Account No. 03-3975 under Order No. 042496-0269250 and proceed to consider this IDS under Rule 97(d).

Consideration of the foregoing and enclosures, plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited, along with an early action on the merits.

Respectfully submitted,
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I certify that this paper is being deposited with the U.S. Postal Service as first class mail in an envelope with postage prepaid, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 12, 2003.


Deanna Costen

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